Amendment filed May 28, 2008 Responding to Office Action mailed February 28, 2008 App. Ser. No. 10/695,390

A BRO operator 422 472 groups bits obtained by dividing the code symbol index k by  $2^m$ , performs a BRO operation on a row index for symbols of each group by the m bits, and calculates a row index  $r_k$  for the code symbol index k.

15/09

13-15

Please amend lines 14-16 of page 40 of the specification to read:

FIG. 14 illustrates a block diagram  $\underline{320e}$  of the deinterleaving address generator 320 generating a read address for reading code symbols written in the input buffer 310, for N<sub>EP</sub>=9240 (m=12 and J=3).

Please amend lines 12-14 of page 41 of the specification to read:

FIG. 15 illustrates a block diagram  $\underline{320f}$  of the deinterleaving address generator 320 generating a read address for reading code symbols written in the input buffer 310, for N<sub>EP</sub>=15384 (m=13 and J=2).